FIG. 1

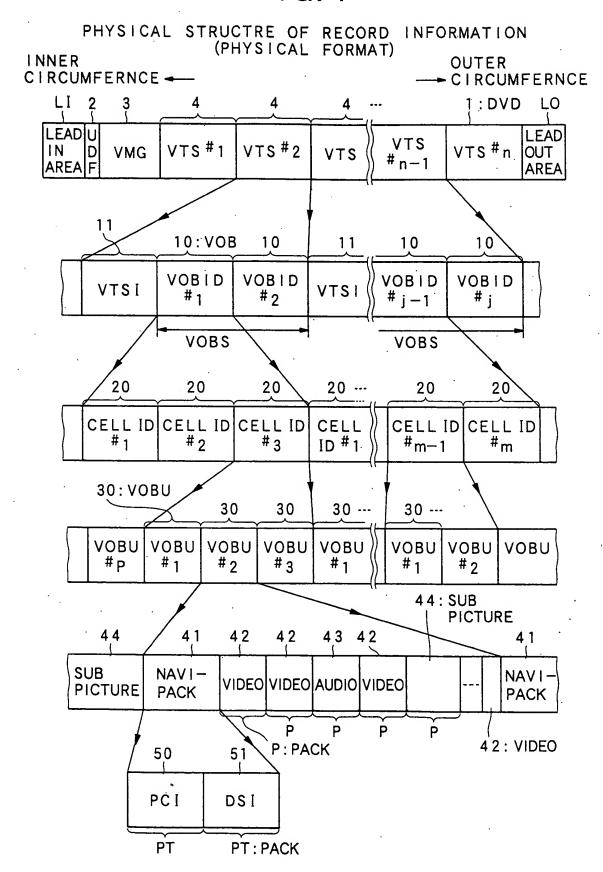


FIG.2

AL STRUCTRE OF RECORD INFORMATION

LOGICAL STRUCTRE OF RECORD INFORMATION (LOGICAL FORMAT)

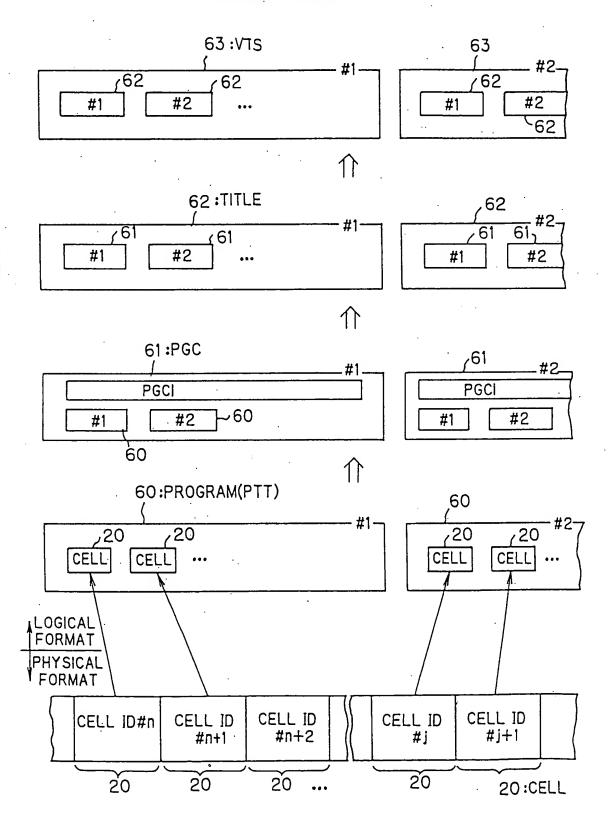


FIG. 3

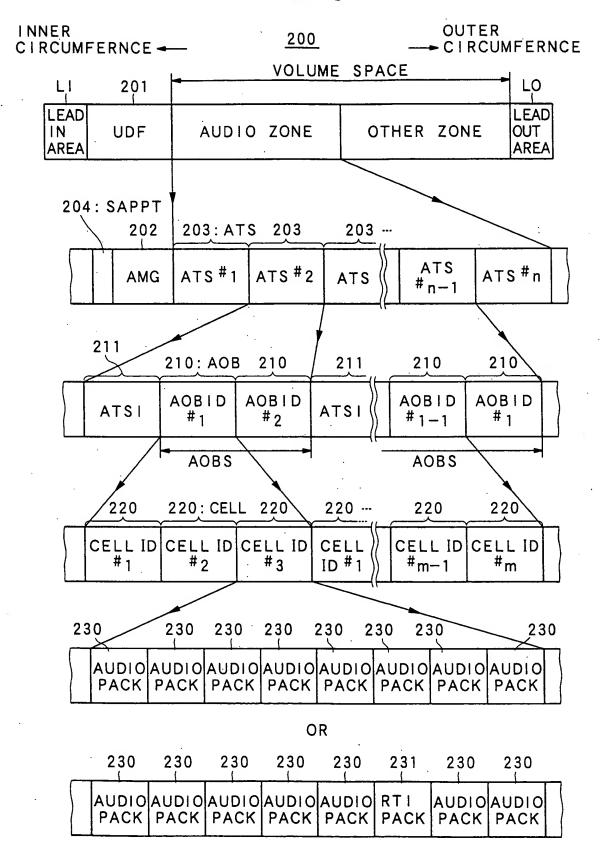


FIG. 4

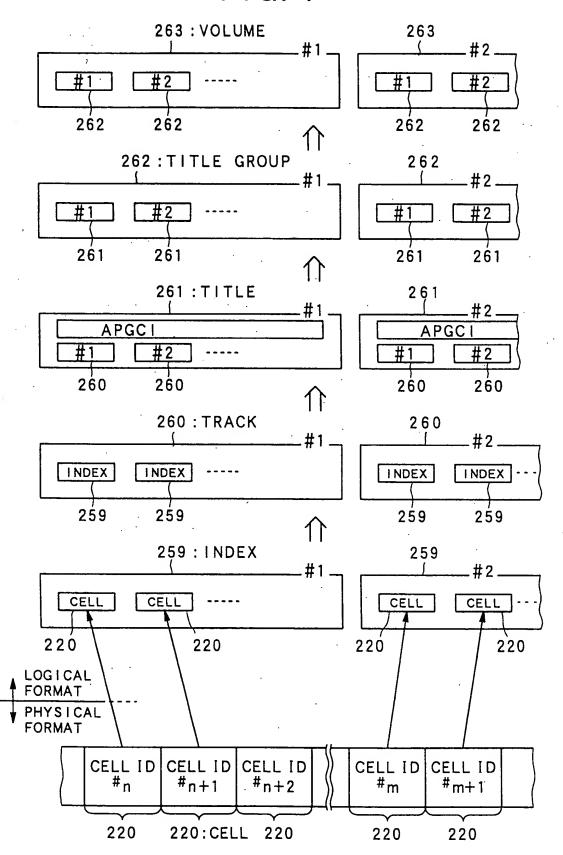
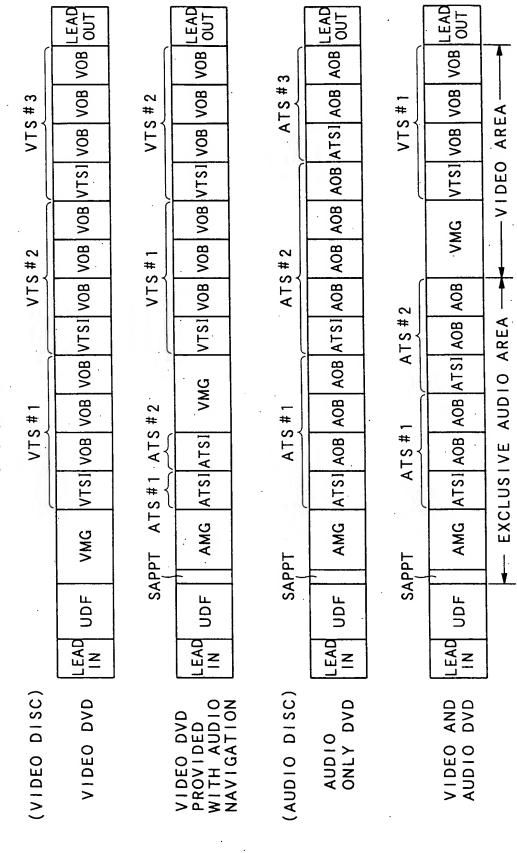


FIG. 5



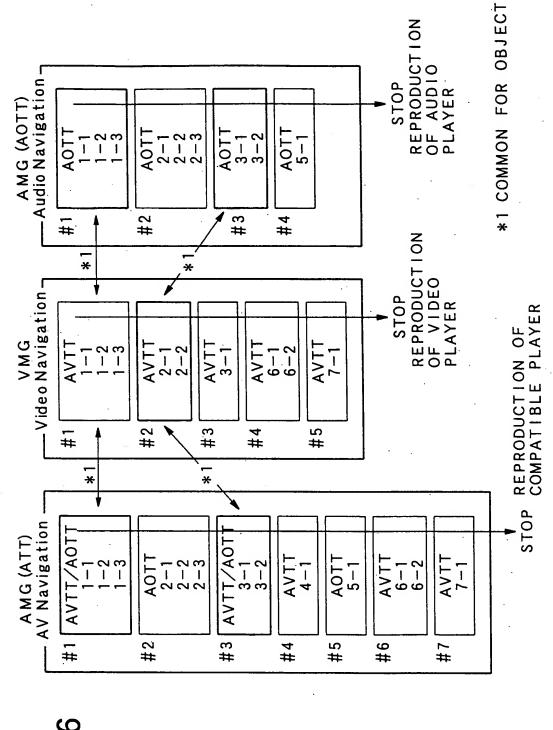
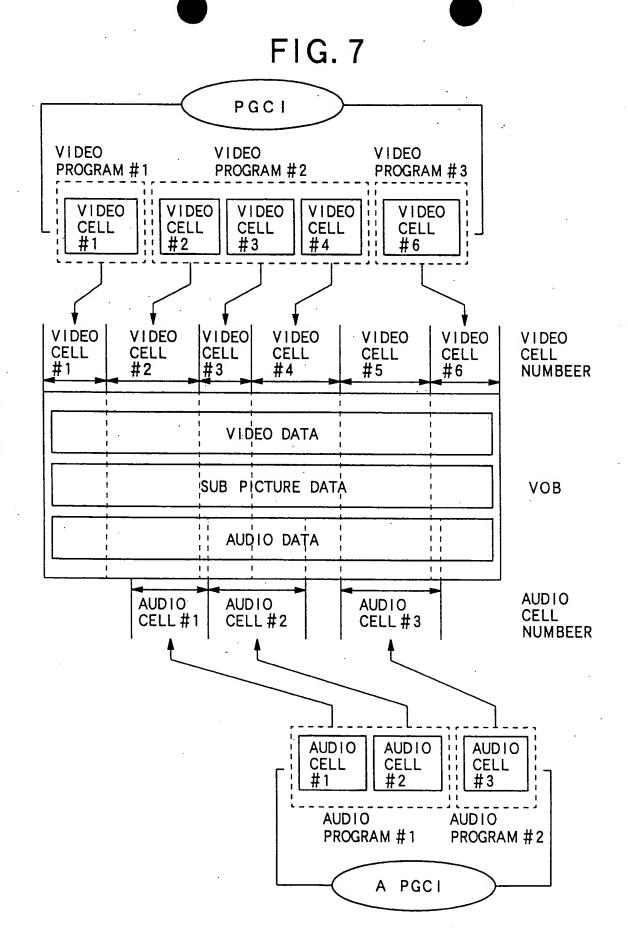
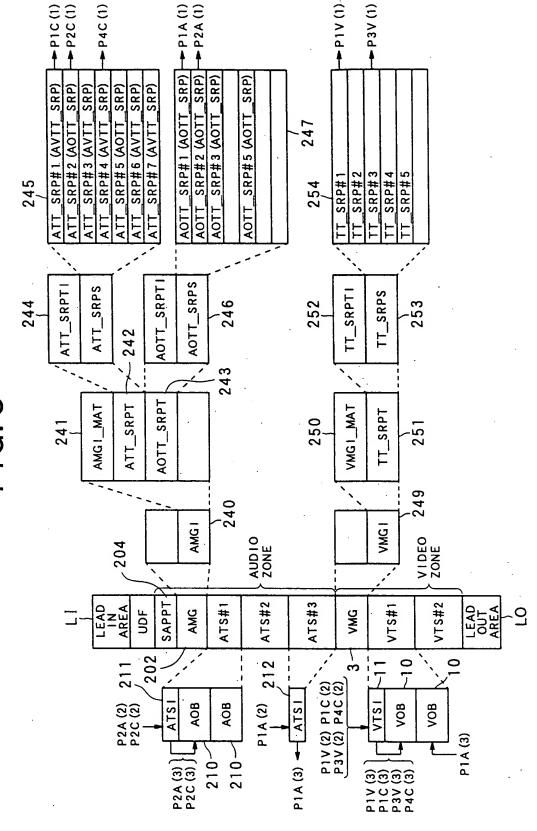


FIG. 6





<u>С</u>

FIG.9A

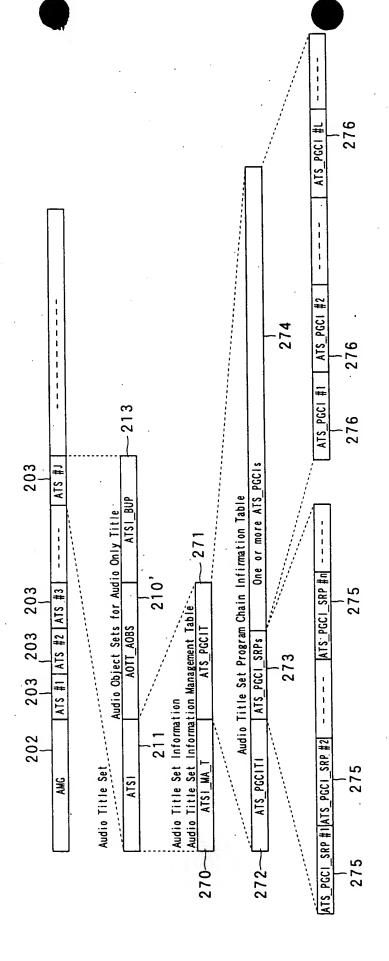


FIG.9B

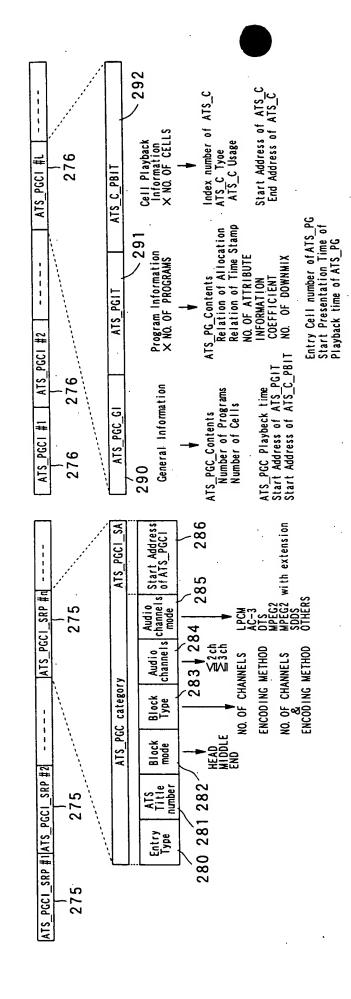
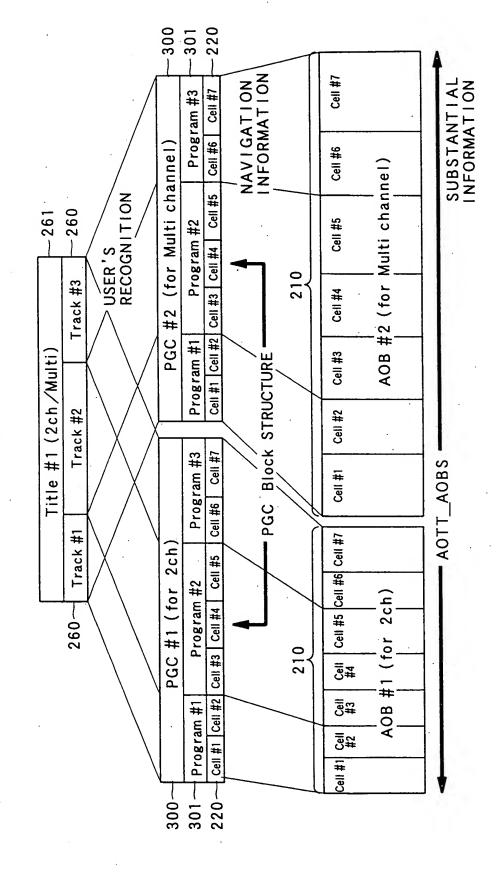


FIG. 10



正 の 二

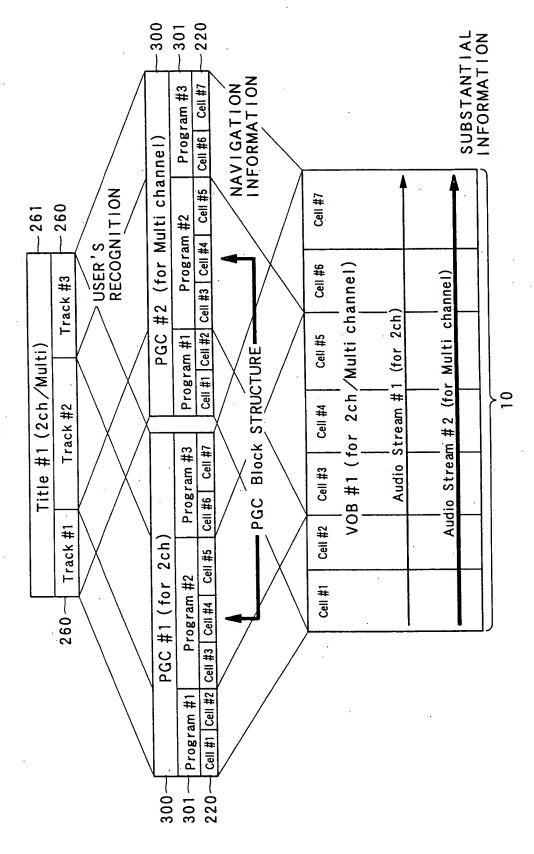
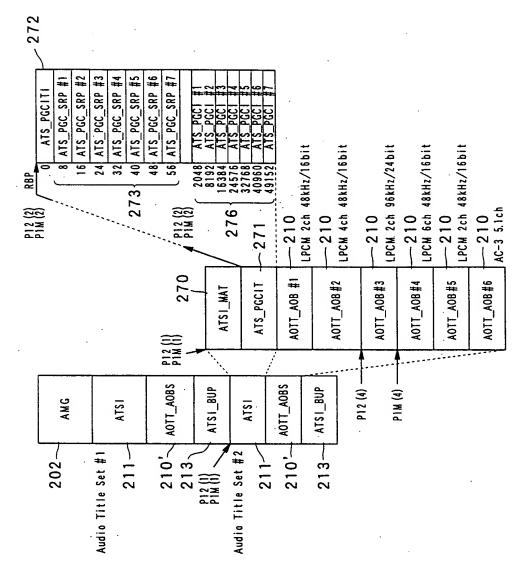


FIG.12A



. . .

FIG.12B

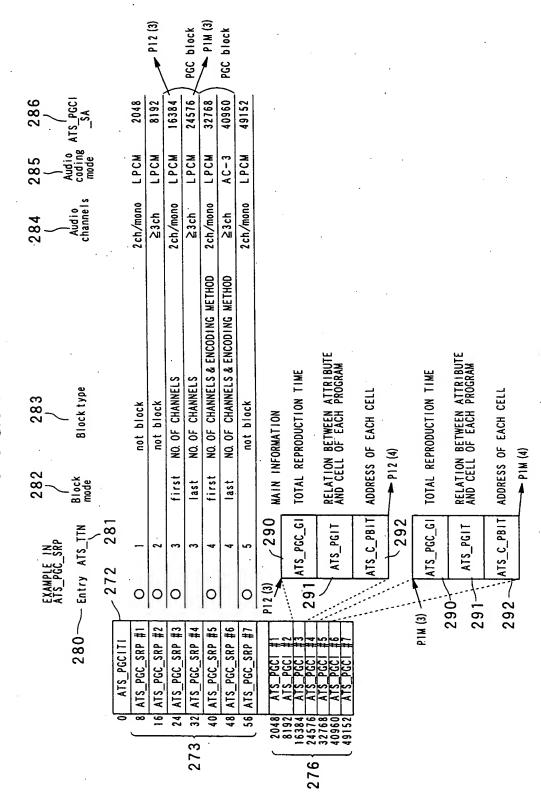


FIG. 13A

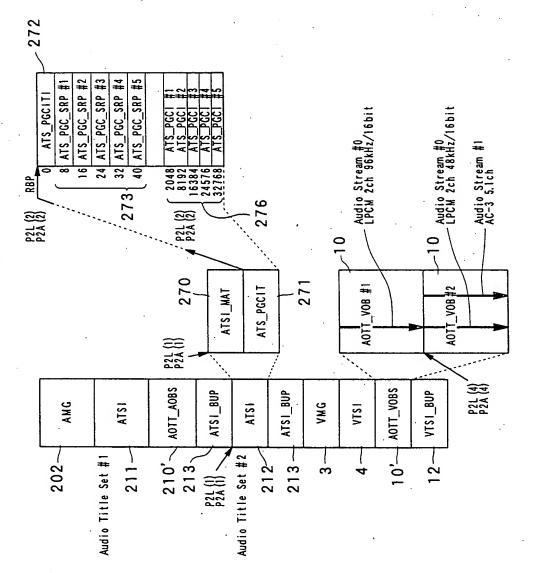
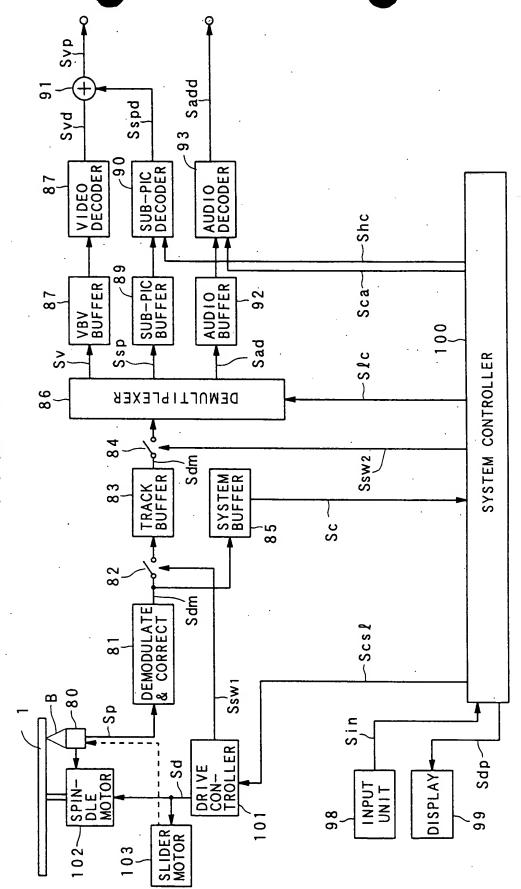


FIG.13B

			P2L (3)	۱	PGC block	P2A (3)	
Audio ATS_PGC1 coding ATS_PGC1 modeSA	2048	8192	16384	24576	32768		
Audio coding mode	LPCM	LPCM	LPCM	LPCM	AC-3		
Audio channe Is	2ch/mono	2ch/mono	2ch/mono	2ch/mono	≧3ch	NO = # 1	
Block mode Block type	not block	not block	not block	first NO. OF CHANNELS & ENCODING METHOD	last NO. OF CHANNELS & ENCODING METHOD	MAIN INFORMATION TOTAL REPRODUCTION TIME RELATION BETWEEN ATTRIBUTE ADDRESS OF EACH CELL TOTAL REPRODUCTION TIME RELATION BETWEEN ATTRIBUTE ADDRESS OF EACH CELL ADDRESS OF EACH CELL ADDRESS OF EACH CELL	F2A (4)
EXAMPLE IN ATS_PGC_SRP . Entry ATS_TTN	0 ATS_PGC1T1 272 8 ATS_PGC_SRP_#1 O 1	#5	273 24 ATS PGC SRP #3 O 3	32 ATS PGC SRP #4		276 16384 ATS_PGC #1 P2L (3) 290 24576 ATS_PGC #3 291 ATS_PGC_G 290 ATS_PGC_G 290 ATS_PGC_G 290 ATS_PGC_G ATS_PG	

FIG. 14



Sadd 96 97 94 93 RTI DECODER BUFFER AUD 10 DECODER AUDIO DECODER BUFFER RTI DECODER 00a MEMORY Sca AUD 10 BUFFER Shc RT! BUFFER 92 95 100 Sad -SIc SYSTEM CONTROLLER 9 **DEMULTIPLEXER** ∞ 84 Ssw₂ Sdm TRACK BUFFER SYSTEM BUFFER 83 Sc 85 82 Sdm DEMODULATE & CORRECT Scsl <u>~</u> Ssw1 -80 Sin 400 DRIVE CON-TROLLER Sdp-PS SPIN-DLE MOTOR DISPLAY HEADPHONE INPUT UNIT 86 99 SLIDER MOTOR 102 103

FIG. 15

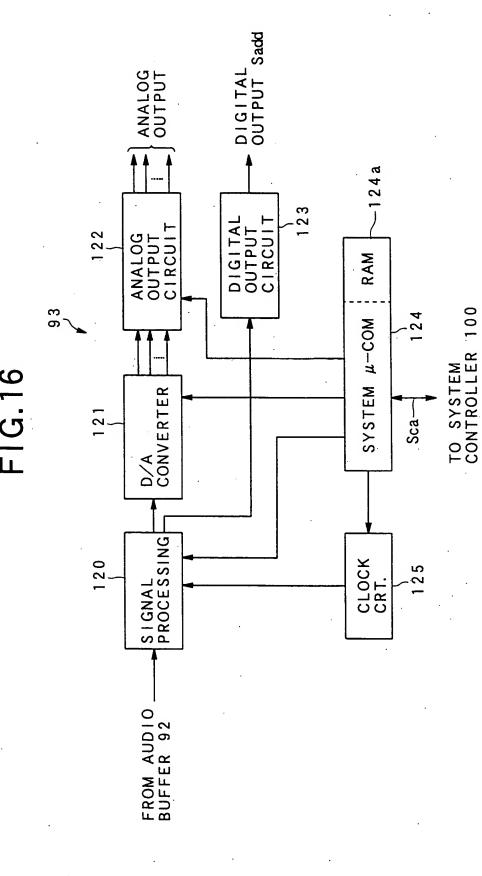
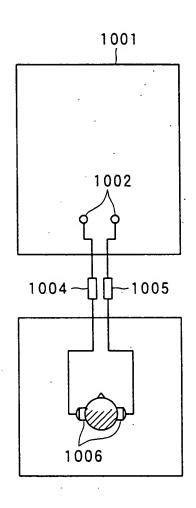


FIG.17A

FIG. 17B



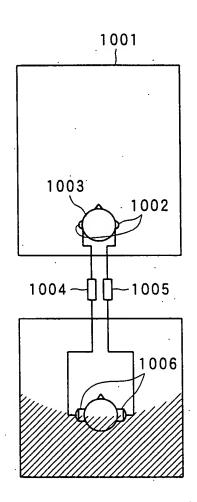


FIG.18

AUDIO PACK 230

#1	#2	#1	#2	#1	#2	#1	
STEREO	BINAÜRAL	STEREO	BINAURAL	STEREO	BINAURAL	STEREO	

FIG.19

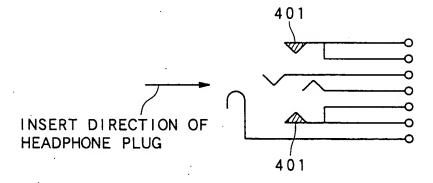


FIG. 20

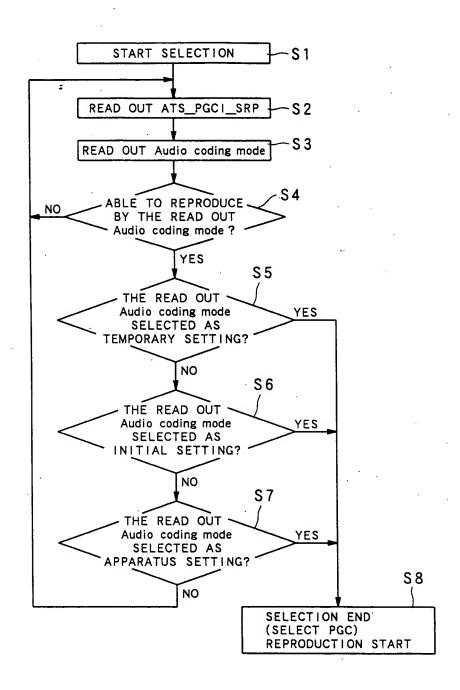


FIG. 21

